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PATENT

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June 28, 2006

Date

Alexandra Beggs

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Kallol Mazumder and Scott

Attorney Docket No.: 501038.03

Smith

Patent No. : US 6,809.970 B2 Issued : October 26, 2004

Title

: INPUT STAGE APPARATUS AND METHOD HAVING A VARIABLE REFERENCE

VOLTAGE

NOTIFICATION OF ERRORS

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

The following errors were noted in a review of the above-identified letters patent. One or more of these errors was inadvertently made in the original application, while the others occurred in the printing of the patent. Since the errors are of an obvious nature, a formal Certificate of Correction is not believed to be warranted at this time. Therefore, applicants request that this notification be placed in the Patent and Trademark Office file.

Column, Line	Reads	Should Read
Item (57), Line 13	"level of the outputs signal"	level of the output signal
Column 1, Line 30	"include both control"	includes both control
Column 4, Line 28	"VREFL <vrefsstl,"< td=""><td>VREFL<vrefsstl< td=""></vrefsstl<></td></vrefsstl,"<>	VREFL <vrefsstl< td=""></vrefsstl<>

Column 5, Line 19	"provide improves noise"	provide improved noise
Column 5, Line 40	"N _{mL,VREFAR} ."	"N _{mL,VREFVAR} ."
Column 8, Line 59	"output node off the input"	output node of the input
Column 10, Line 51	"buffer having"	buffer having an
Column 10, Line 54	"to which a initial"	to which an initial
Column 13, Line 5	"output node off the input"	output node of the input
Column 13, Line 65	"comprise a SSTL input"	comprise an SSTL input
Column 14, Line 62	"input signal terminal,"	an input signal terminal,
Column 14, Line 64	"to which a initial"	to which an initial

Respectfully submitted,

Date: June 26, 2006

By:

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Enclosure:

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